



-1-

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

13/C
(ve)

In re Patent Application of)	Group Art Unit: 2811
Ronald Pasqualini)	Examiner: O. Nadav
Appln. No. 09/164,216)	<u>RESPONSE UNDER 37 CFR</u>
Filed: September 30, 1998)	<u>\$1.116, EXPEDITED</u>
For: <u>ESD PROTECTION CIRCUIT</u>)	<u>PROCEDURE REQUESTED</u>
<u>UTILIZING FLOATING LATERAL</u>)	2001 Ferry Building
<u>CLAMP DIODES</u>)	San Francisco, CA 94111
)	(415) 433-4150

Commissioner for Patents
Washington, D.C. 20231

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on November 20, 2000.

LIMBACH & LIMBACH L.L.P.

Date: November 20, 2000

By:

Name: Norma E. Gillespie

Norma E. Gillespie

Sir:

In response to the Official Action mailed October 11, 2000, please amend the above-identified application as follows:

In the Claims

Please amend the claims as follows:

1. (Twice Amended) A semiconductor chip having a substrate of a first conductivity type, the chip comprising:
 - a pad;
 - an electrostatic discharge (ESD) positive line, the ESD positive line not being connected to a steady voltage source;
 - an ESD negative line; and

do not
enter
on

RECEIVED
DEC - 4 2000
TC 2800 MAIL ROOM